

Product Overview

MC14043B: Quad R-S Latches

For complete documentation, see the data sheet.

The MC14043B and MC14044B quad R-S latches are constructed with MOS P-channel and N-channel enhancement mode devices in a single monolithic structure. Each latch has an independent Q output and set and reset inputs. The Q outputs are gated through three-state buffers having a common enable input. The outputs are enabled with a logical "1" or high on the enable input; a logical "0" or low disconnects the latch from the Q outputs, resulting in an open circuit at the Q outputs.

Features

- Double Diode Input Protection
- Three-State Outputs with Common Enable
- Outputs Capable of Driving Two Low-power TTL Loads or One Low-Power Schottky TTL Load Over the Rated Temperature Range
- Supply Voltage Range = 3.0 Vdc to 18 Vdc
- Pb-Free Packages are Available*

Part Electrical Specifications

| Product | Compliance | Status | Type | Channels | V _{CC} Min (V) | V _{CC} Max (V) | t _{pd} Max (ns) | I _O Max (mA) | Package Type |
|---------------|---------------|--------|-------|----------|-------------------------|-------------------------|--------------------------|-------------------------|--------------|
| MC14043BDG | Pb-free | Active | Latch | 4 | 3 | 18 | 175 | 2.25 | SOIC-16 |
| | Halide free | | | | | | | | |
| MC14043BDR2G | Pb-free | Active | Latch | 4 | 3 | 18 | 175 | 2.25 | SOIC-16 |
| | Halide free | | | | | | | | |
| NLV14043BDG | AEC Qualified | Active | Latch | 4 | 3 | 18 | 175 | 2.25 | SOIC-16 |
| | PPAP Capable | | | | | | | | |
| | Pb-free | | | | | | | | |
| | Halide free | | | | | | | | |
| NLV14043BDR2G | AEC Qualified | Active | Latch | 4 | 3 | 18 | 175 | 2.25 | SOIC-16 |
| | PPAP Capable | | | | | | | | |
| | Pb-free | | | | | | | | |
| | Halide free | | | | | | | | |

For more information please contact your local sales support at www.onsemi.com.

Created on: 6/18/2019