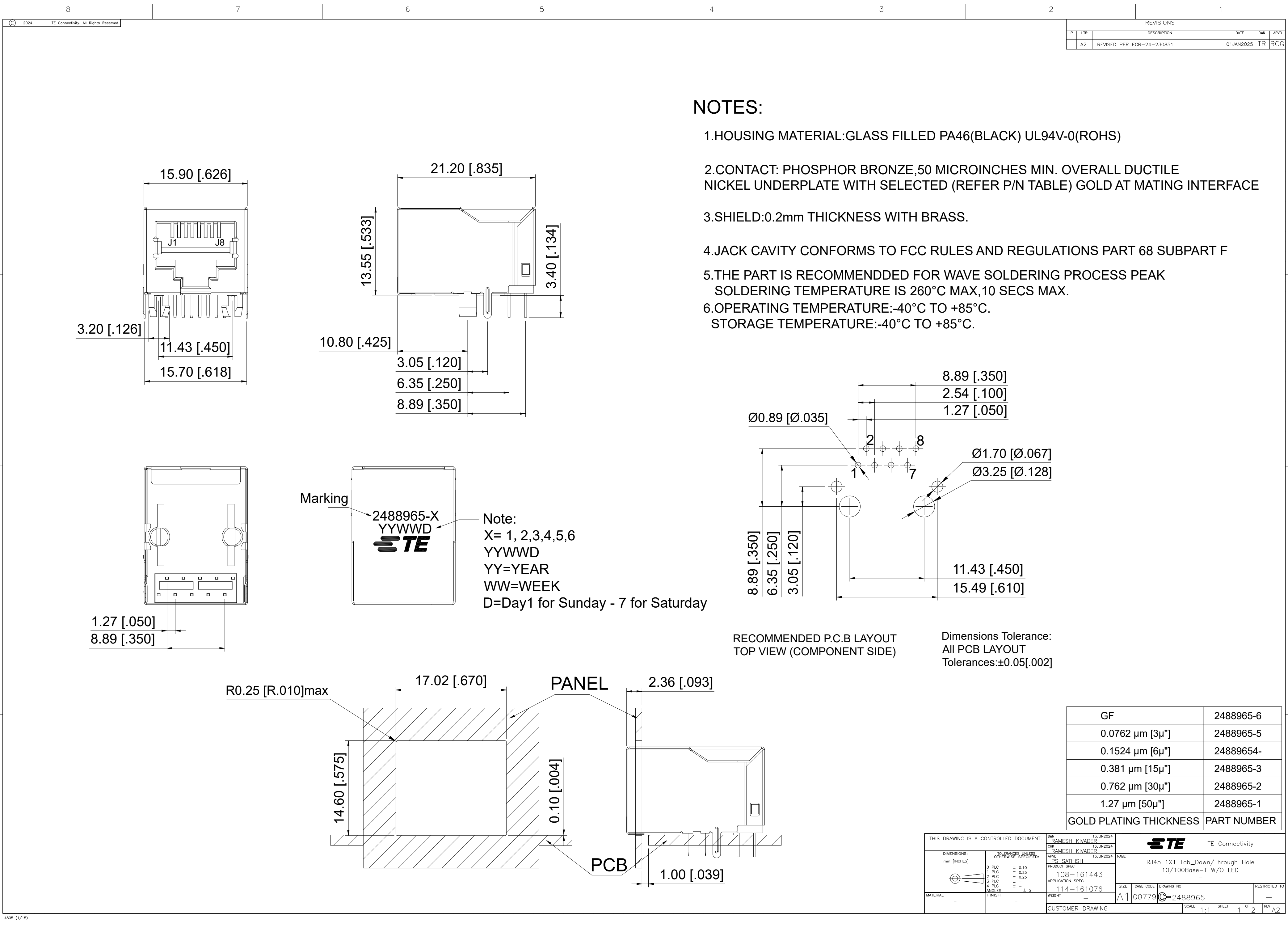
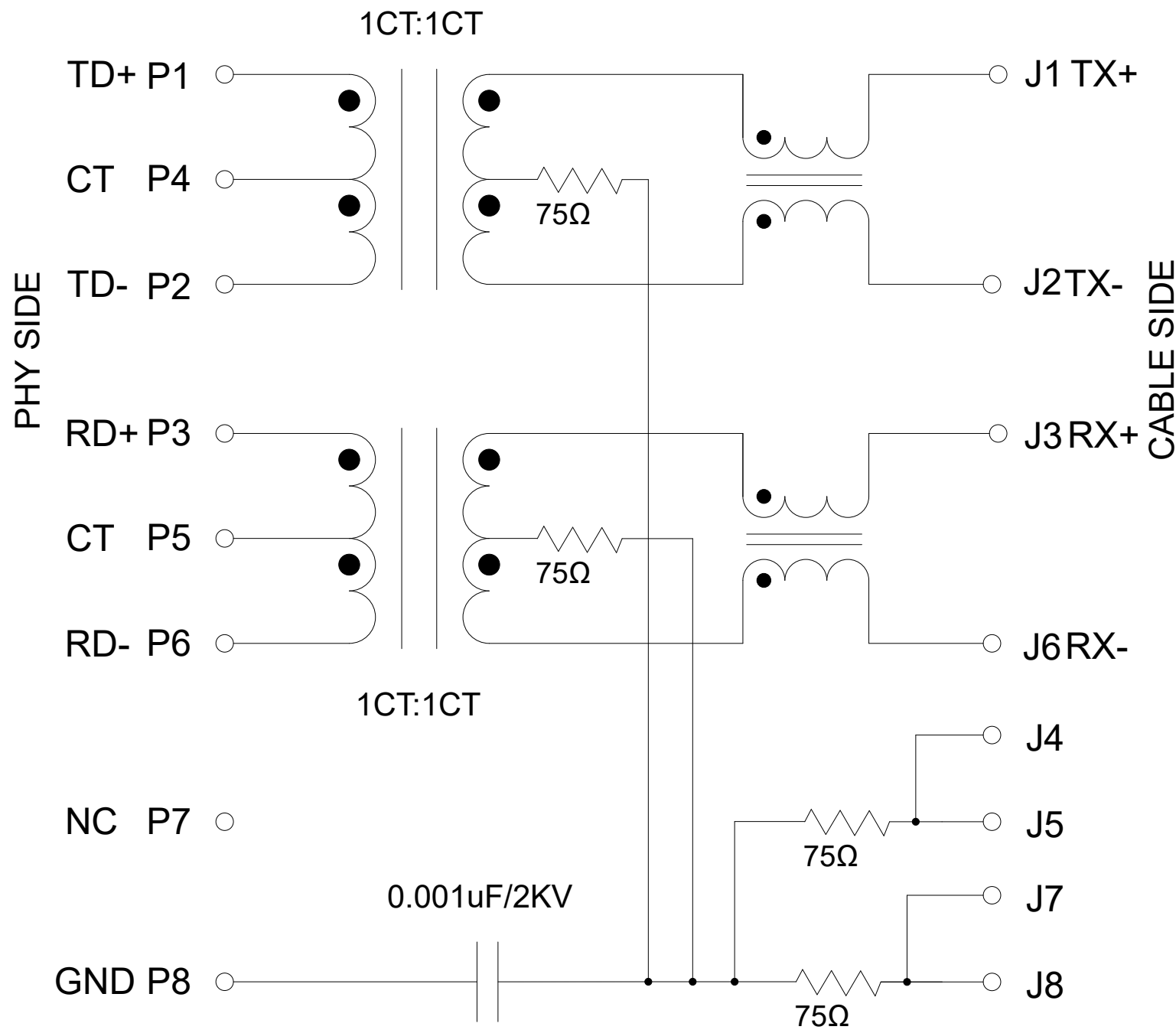




Schematic



PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-40°C To +85°C
URNS RATIO	1:1±2%
INDUCTANCE(OCL)	350uH MIN@100KHz/100mV With 8mA DC Bias
INSERTION LOSS	-1.0dB MAX@0.3MHz-100MHz;
RETURN LOSS (Z out=100 OHM)	-18dB MIN@1MHz-30MHz; -16dB MIN@30MHz-60MHz; -12dB MIN@60MHz-80MHz;-10dB MIN@80MHz-100MHz;
CROSSTALK (ADJACENT CHANNELS)	-30dB MIN@1MHz-100MHz;
COMMON MODE REJECTION RATIO	-30dB MIN@1MHz-100MHz;
HI-POT	2250 VDC@60 SECONDS

THIS DRAWING IS A CONTROLLED DOCUMENT.				TE Connectivity			
DIMENSIONS: mm [INCHES]				RJ45 1X1 Tab_Down/Through Hole 10/100Base-T W/O LED			
TOLERANCES UNLESS OTHERWISE SPECIFIED:				RESTRICTED TO			
0 PLC ± 0.10 1 PLC ± 0.25 2 PLC ± 0.25 3 PLC ± - 4 PLC ± - ANGLES ± 2				SCALE 1:1 SHEET 2 OF 2 REV A2			
MATERIAL -				CUSTOMER DRAWING			