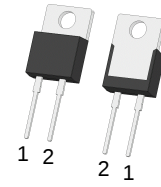


# Silicon Carbide (SiC) Diode – EliteSiC, TO220-2, 2 A, 1200 V SiC Merged PiN-Schottky (MPS) Diode UJ3D1202TS



TO220-2  
CASE 340AZ

## Description

onsemi offers the 3<sup>rd</sup> generation of high performance SiC Merged-PiN-Schottky (MPS) diodes. With zero reverse recovery charge and 175 °C maximum junction temperature, these diodes are ideally suited for high frequency and high efficiency power systems with minimum cooling requirements.

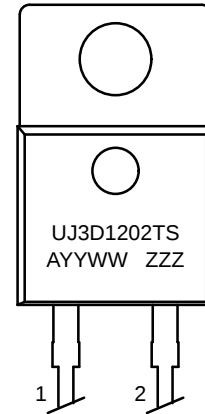
## Features

- Maximum Operating Temperature of 175 °C
- Easy Paralleling
- Extremely Fast Switching not Dependent on Temperature
- No Reverse or Forward Recovery
- Enhanced Surge Current Capability, MPS Structure
- Excellent Thermal Performance, Ag Sintered
- 100% UIS Tested
- This Device is Pb-Free, Halogen Free and is ROHS Compliant

## Typical Applications

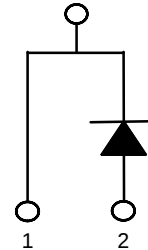
- Power Converters
- Industrial Motor Drives
- Switch Mode Power Supplies
- Power Factor Correction Modules

## MARKING DIAGRAM



|            |                        |
|------------|------------------------|
| UJ3D1202TS | = Specific Device Code |
| A          | = Assembly Location    |
| YY         | = Year                 |
| WW         | = Work Week            |
| ZZZ        | = Lot ID               |

## PIN CONNECTIONS



## ORDERING INFORMATION

See detailed ordering and shipping information on page 4 of this data sheet.

# UJ3D1202TS

## MAXIMUM RATINGS

| Parameter                                                           | Symbol         | Test Conditions                                                     | Value      | Unit                 |
|---------------------------------------------------------------------|----------------|---------------------------------------------------------------------|------------|----------------------|
| DC Blocking Voltage                                                 | $V_R$          |                                                                     | 1200       | V                    |
| Repetitive Peak Reverse Voltage, $T_J = 25\text{ }^{\circ}\text{C}$ | $V_{RRM}$      |                                                                     | 1200       | V                    |
| Surge Peak Reverse Voltage                                          | $V_{RSM}$      |                                                                     | 1200       | V                    |
| Maximum DC Forward Current                                          | $I_F$          | $T_C = 164\text{ }^{\circ}\text{C}$                                 | 2          | A                    |
| Non-repetitive Forward Surge Current Sine Halfwave                  | $I_{FSM}$      | $T_C = 25\text{ }^{\circ}\text{C}$ , $t_p = 10\text{ ms}$           | 30         | A                    |
|                                                                     |                | $T_C = 110\text{ }^{\circ}\text{C}$ , $t_p = 10\text{ ms}$          | 27         |                      |
| Repetitive Forward Surge Current Sine Halfwave, $D = 0.1$           | $I_{FRM}$      | $T_C = 25\text{ }^{\circ}\text{C}$ , $t_p = 10\text{ ms}$           | 14.8       | A                    |
|                                                                     |                | $T_C = 110\text{ }^{\circ}\text{C}$ , $t_p = 10\text{ ms}$          | 8.8        |                      |
| Non-repetitive Peak Forward Current                                 | $I_{F,max}$    | $T_C = 25\text{ }^{\circ}\text{C}$ , $t_p = 10\text{ }\mu\text{s}$  | 250        | A                    |
|                                                                     |                | $T_C = 110\text{ }^{\circ}\text{C}$ , $t_p = 10\text{ }\mu\text{s}$ | 250        |                      |
| $i^2t$ Value                                                        | $\int i^2 dt$  | $T_C = 25\text{ }^{\circ}\text{C}$ , $t_p = 10\text{ ms}$           | 4.5        | $\text{A}^2\text{s}$ |
|                                                                     |                | $T_C = 110\text{ }^{\circ}\text{C}$ , $t_p = 10\text{ ms}$          | 3.6        |                      |
| Power Dissipation                                                   | $P_{tot}$      | $T_C = 25\text{ }^{\circ}\text{C}$                                  | 75         | W                    |
|                                                                     |                | $T_C = 164\text{ }^{\circ}\text{C}$                                 | 5.5        |                      |
| Maximum Junction Temperature                                        | $T_{J,max}$    |                                                                     | 175        | $^{\circ}\text{C}$   |
| Operating and Storage Temperature                                   | $T_J, T_{STG}$ |                                                                     | -55 to 175 | $^{\circ}\text{C}$   |
| Soldering Temperatures, Wavesoldering only Allowed at Leads         | $T_{sold}$     | 1.6 mm from case for 10 s                                           | 260        | $^{\circ}\text{C}$   |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## THERMAL CHARACTERISTICS

| Parameter                            | Symbol          | Test Conditions | Min | Typ | Max | Unit                 |
|--------------------------------------|-----------------|-----------------|-----|-----|-----|----------------------|
| Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ |                 | –   | 1.5 | 2   | $^{\circ}\text{C/W}$ |

## ELECTRICAL CHARACTERISTICS ( $T_J = +25\text{ }^{\circ}\text{C}$ unless otherwise specified)

| Parameter                        | Symbol | Test Conditions                                             | Min | Typ  | Max | Unit          |
|----------------------------------|--------|-------------------------------------------------------------|-----|------|-----|---------------|
| Forward Voltage                  | $V_F$  | $I_F = 2\text{ A}$ , $T_J = 25\text{ }^{\circ}\text{C}$     | –   | 1.4  | 1.6 | V             |
|                                  |        | $I_F = 2\text{ A}$ , $T_J = 150\text{ }^{\circ}\text{C}$    | –   | 1.85 | 2.3 |               |
|                                  |        | $I_F = 2\text{ A}$ , $T_J = 175\text{ }^{\circ}\text{C}$    | –   | 2    | 2.6 |               |
| Reverse Current                  | $I_R$  | $V_R = 1200\text{ V}$ , $T_J = 25\text{ }^{\circ}\text{C}$  | –   | 2    | 22  | $\mu\text{A}$ |
|                                  |        | $V_R = 1200\text{ V}$ , $T_J = 175\text{ }^{\circ}\text{C}$ | –   | 60   | –   |               |
| Total Capacitive Charge (Note 1) | $Q_C$  | $V_R = 800\text{ V}$                                        | –   | 12   | –   | nC            |
| Total Capacitance                | $C$    | $V_R = 1\text{ V}$ , $f = 1\text{ MHz}$                     | –   | 109  | –   | pF            |
|                                  |        | $V_R = 400\text{ V}$ , $f = 1\text{ MHz}$                   | –   | 11.5 | –   |               |
|                                  |        | $V_R = 800\text{ V}$ , $f = 1\text{ MHz}$                   | –   | 9.8  | –   |               |
| Capacitance Stored Energy        | $E_C$  | $V_R = 800\text{ V}$                                        | –   | 3.6  | –   | $\mu\text{J}$ |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1.  $Q_C$  is independent on  $T_J$ ,  $di_F/dt$ , and  $I_F$  as shown in the application note [AND90316/D](#)

TYPICAL PERFORMANCE DIAGRAMS

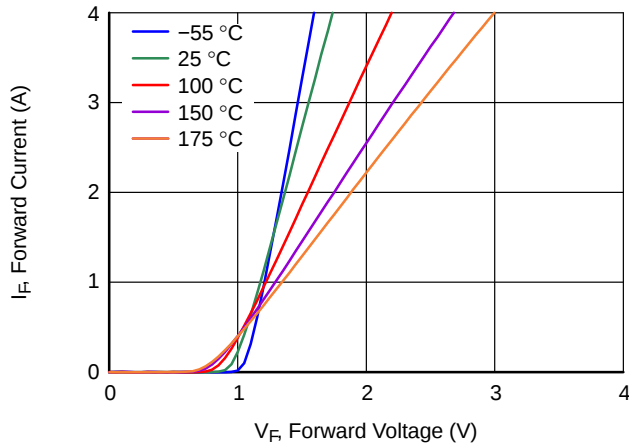


Figure 1. Typical Forward Characteristics

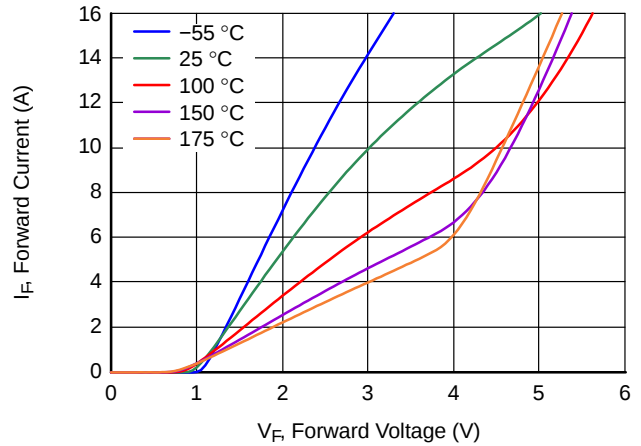


Figure 2. Typical Forward Characteristics in Surge Current

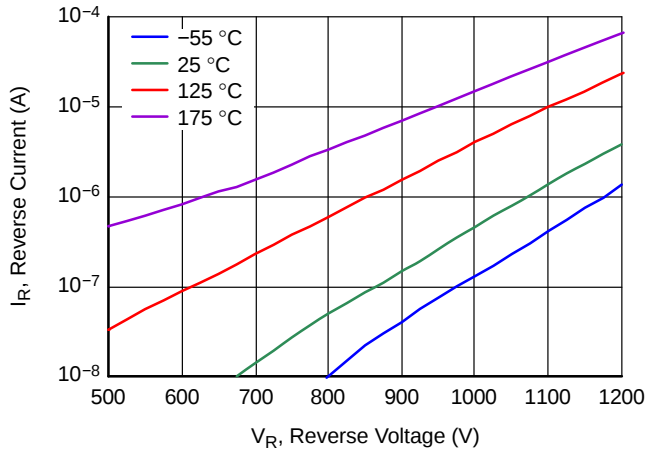


Figure 3. Typical Reverse Characteristics

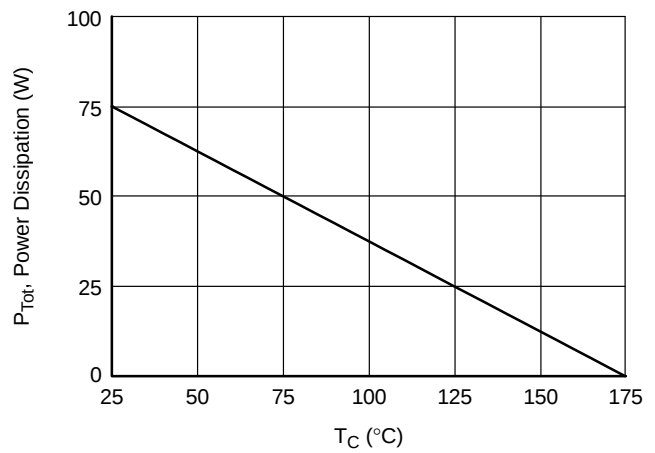


Figure 4. Power Dissipation

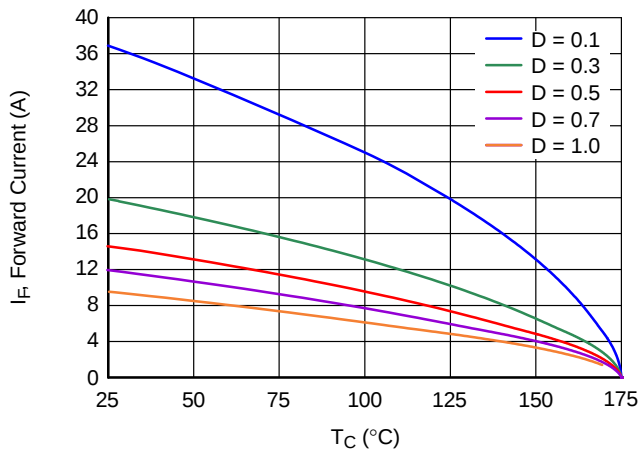


Figure 5. Diode Forward Current

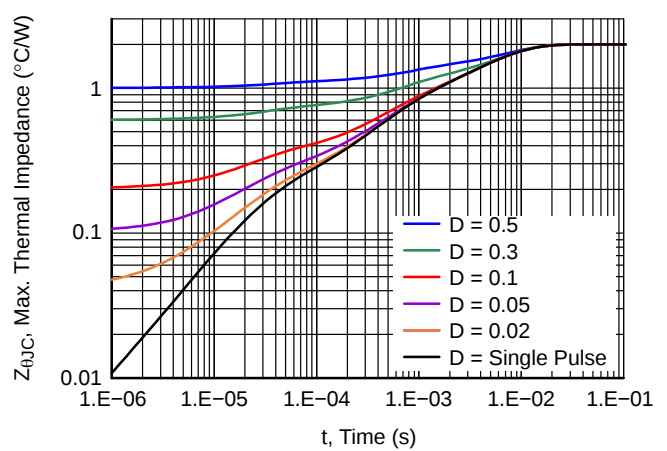
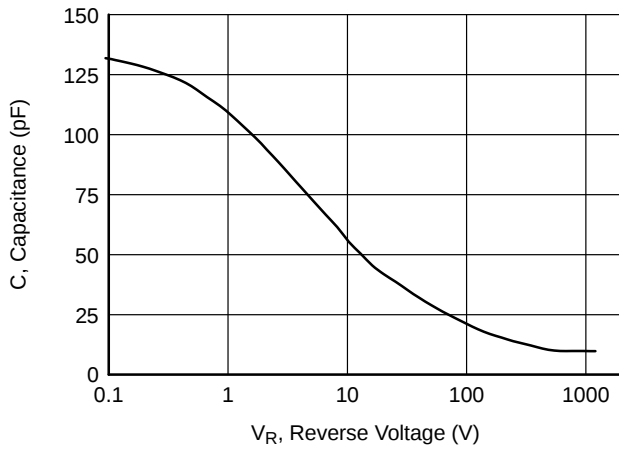


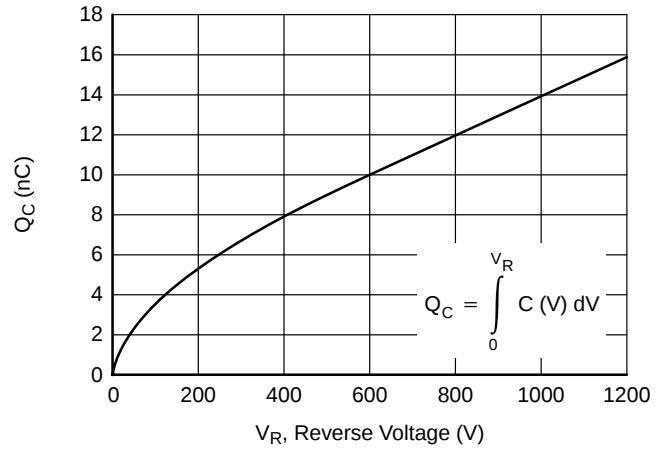
Figure 6. Maximum Transient Thermal Impedance

# UJ3D1202TS

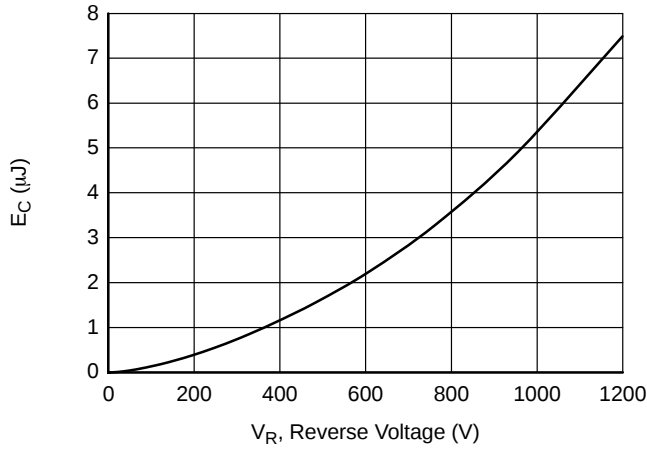
## TYPICAL PERFORMANCE DIAGRAMS (CONTINUED)



**Figure 7. Capacitance vs. Reverse Voltage at 1 MHz**



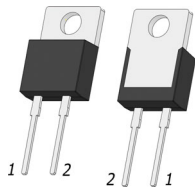
**Figure 8. Typical Capacitive Charge vs. Reverse Voltage**



**Figure 9. Typical Capacitance Stored Energy vs. Reverse Voltage**

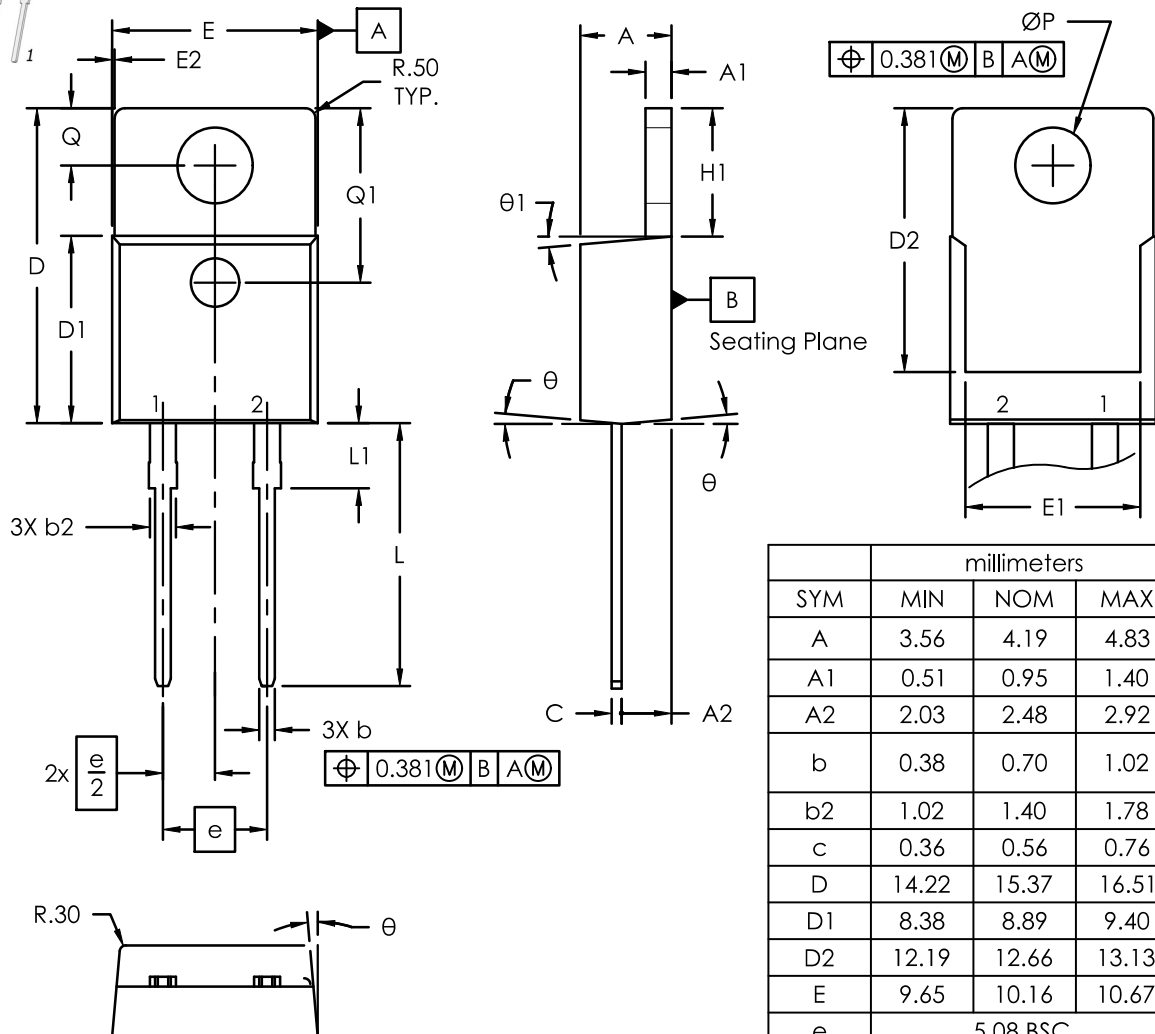
### ORDERING INFORMATION

| Part Number | Marking    | Package                            | Shipping    |
|-------------|------------|------------------------------------|-------------|
| UJ3D1202TS  | UJ3D1202TS | TO220-2<br>(Pb-Free, Halogen Free) | 1000 / Tube |



**TO220-2 10.16x15.37x4.19, 5.08P**  
**CASE 340AZ**  
**ISSUE B**

DATE 23 APR 2025



**NOTES:**

1. Dimensioning and Tolerancing as per ASME Y14.5M, 2018.
2. Controlling Dimension : Millimeters
3. Dimensions D and E does not include Mold Flash. These dimensions are measure at the outermost extreme of the plastic body.
4. Through hole diameter value = End Hole Diameter
5. PCB through hole pattern as per IPC-2222

|            | millimeters |       |       |
|------------|-------------|-------|-------|
| SYM        | MIN         | NOM   | MAX   |
| A          | 3.56        | 4.19  | 4.83  |
| A1         | 0.51        | 0.95  | 1.40  |
| A2         | 2.03        | 2.48  | 2.92  |
| b          | 0.38        | 0.70  | 1.02  |
| b2         | 1.02        | 1.40  | 1.78  |
| c          | 0.36        | 0.56  | 0.76  |
| D          | 14.22       | 15.37 | 16.51 |
| D1         | 8.38        | 8.89  | 9.40  |
| D2         | 12.19       | 12.66 | 13.13 |
| E          | 9.65        | 10.16 | 10.67 |
| e          | 5.08 BSC.   |       |       |
| E1         | 6.86        | 7.87  | 8.89  |
| E2         | —           | —     | 0.76  |
| L          | 12.57       | 13.65 | 14.73 |
| L1         | —           | —     | 6.35  |
| $\phi$ P   | 3.53        | 3.81  | 4.09  |
| H1         | 5.84        | 6.35  | 6.86  |
| Q          | 2.54        | 2.98  | 3.43  |
| Q1         | 8.38        | 8.51  | 8.64  |
| $\theta$   | 5°          |       |       |
| $\theta$ 1 | 5°          |       |       |

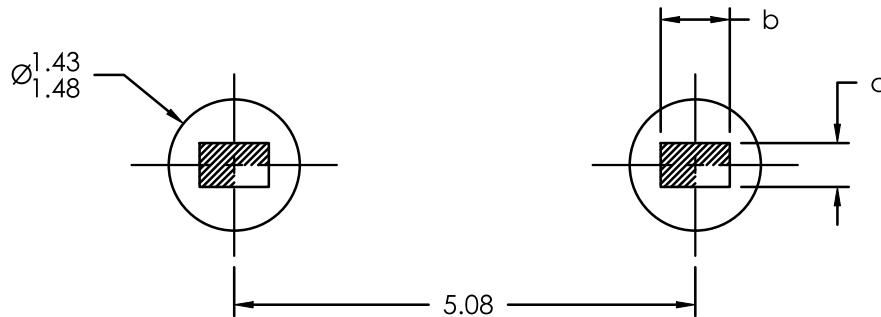
|                         |                                        |                                                                                                                                                                                     |
|-------------------------|----------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>DOCUMENT NUMBER:</b> | <b>98AON13830G</b>                     | Electronic versions are uncontrolled except when accessed directly from the Document Repository.<br>Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| <b>DESCRIPTION:</b>     | <b>TO220-2 10.16x15.37x4.19, 5.08P</b> | <b>PAGE 1 OF 2</b>                                                                                                                                                                  |

onsemi and onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

TO220-2 10.16x15.37x4.19, 5.08P  
CASE 340AZ  
ISSUE B

DATE 23 APR 2025

RECOMMENDED PCB PATTERN



NOTE: LAND PATTERN AND THROUGH HOLE DIMENSIONS SERVE ONLY AS AN INITIAL GUIDE.  
END-USER PCB DESIGN RULES AND TOLERANCES SHOULD ALWAYS PREVAIL.

|                         |                                        |                                                                                                                                                                                     |
|-------------------------|----------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>DOCUMENT NUMBER:</b> | <b>98AON13830G</b>                     | Electronic versions are uncontrolled except when accessed directly from the Document Repository.<br>Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| <b>DESCRIPTION:</b>     | <b>TO220-2 10.16x15.37x4.19, 5.08P</b> | <b>PAGE 2 OF 2</b>                                                                                                                                                                  |

**onsemi** and **onsemi** are trademarks of Semiconductor Components Industries, LLC dba **onsemi** or its subsidiaries in the United States and/or other countries. **onsemi** reserves the right to make changes without further notice to any products herein. **onsemi** makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. **onsemi** does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## ADDITIONAL INFORMATION

### TECHNICAL PUBLICATIONS:

Technical Library: [www.onsemi.com/design/resources/technical-documentation](http://www.onsemi.com/design/resources/technical-documentation)  
onsemi Website: [www.onsemi.com](http://www.onsemi.com)

### ONLINE SUPPORT: [www.onsemi.com/support](http://www.onsemi.com/support)

For additional information, please contact your local Sales Representative at  
[www.onsemi.com/support/sales](http://www.onsemi.com/support/sales)