

# X67BC8331

## 1 General information

### 1.1 Other applicable documents

For additional and supplementary information, see the following documents.

#### Other applicable documents

Document name	Title
MAX67	<a href="#">X67 system user's manual</a>
MAEMV	<a href="#">Installation / EMC guide</a>

### 1.2 Order data


Order number	Short description	Figure
	<b>Bus controller modules</b>	
X67BC8331	X67 bus controller, 1 POWERLINK interface, X2X Link power supply 3 W, 8 digital channels configurable as inputs or outputs, 24 VDC, 2 A, configurable input filter	

Table 1: X67BC8331 - Order data

Required accessories
See "Required cables and connectors" on page 9. For a general overview, see section "Accessories - General overview" in the X67 system user's manual.

### 1.3 Module description

The bus controller makes it possible to connect X2X Link I/O nodes to POWERLINK. It is also possible to operate the X2X Link cycle synchronously 1:1 or synchronous to POWERLINK using a prescaler.

Additional X2X Link I/O nodes (X67 modules or other modules based on X2X Link) can be connected using the integrated X2X Link connection. Mechanically, POWERLINK is connected via an IP67-protected standard D-coded M12 Ethernet connector.

Functions:

- [POWERLINK](#)
- [Digital inputs](#)
- [Digital outputs](#)

#### POWERLINK

POWERLINK is a standard protocol for Fast Ethernet equipped with hard real-time characteristics.

#### Digital inputs

The digital inputs are equipped with an input filter with a configurable input delay.

#### Monitoring status of the digital outputs

The output signal of the digital outputs is monitored for short circuit or overload.

## 2 Technical description

### 2.1 Technical data

<b>Order number</b>	<b>X67BC8331</b>
<b>Short description</b>	
Bus controller	POWERLINK (V1/V2) controlled node
<b>General information</b>	
Inputs/Outputs	8 digital channels, configurable as inputs or outputs using software, inputs with additional functions
Insulation voltage between channel and bus	500 V <sub>eff</sub>
Nominal voltage	24 VDC
B&R ID code	
Bus controller	0xA7A5
Internal I/O module	0x1311
Sensor/Actuator power supply	0.5 A summation current
Status indicators	I/O function per channel, supply voltage, bus function
Diagnostics	
Outputs	Yes, using LED status indicator and software
I/O power supply	Yes, using LED status indicator and software
Support	
Dynamic node allocation (DNA)	Yes
Connection type	
Fieldbus	M12, D-coded
X2X Link	M12, B-coded
Inputs/Outputs	8x M8, 3-pin
I/O power supply	M8, 4-pin
Power output	3 W X2X Link power supply for I/O modules
Power consumption	
Fieldbus	3.5 W
Internal I/O	3.8 W
X2X Link power supply	4.2 W at maximum power output for connected I/O modules
Certifications	
CE	Yes
UKCA	Yes
ATEX	Zone 2, II 3G Ex nA IIA T5 Gc IP67, Ta = 0 - Max. 60°C TÜV 05 ATEX 7201X
UL	cULus E115267 Industrial control equipment
HazLoc	cCSAus 244665 Process control equipment for hazardous locations Class I, Division 2, Groups ABCD, T5
EAC	Yes
KC	Yes
<b>Interfaces</b>	
Fieldbus	POWERLINK (V1/V2) controlled node
Type	Type 2 <sup>1)</sup>
Variant	M12 interface (female connector on module)
Line length	Max. 100 m between 2 stations (segment length)
Transfer rate	100 Mbit/s
Transfer	
Physical layer	100BASE-TX
Half-duplex	Yes
Full-duplex	No
Autonegotiation	Yes
Auto-MDI/MDIX	Yes
Min. cycle time <sup>2)</sup>	
Fieldbus	200 µs
X2X Link	200 µs
Synchronization between bus systems possible	Yes
<b>I/O power supply</b>	
Nominal voltage	24 VDC
Voltage range	18 to 30 VDC
Integrated protection	Reverse polarity protection
Power consumption	
Sensor/Actuator power supply	Max. 12 W <sup>3)</sup>
<b>Sensor/Actuator power supply</b>	
Voltage	I/O power supply minus voltage drop for short-circuit protection
Voltage drop for short-circuit protection at 0.5 A	Max. 2 VDC
Summation current	Max. 0.5 A
Short-circuit proof	Yes

Table 2: X67BC8331 - Technical data

Order number	X67BC8331
<b>Digital inputs</b>	
Input characteristics per EN 61131-2	Type 1
Input voltage	18 to 30 VDC
Input current at 24 VDC	Typ. 4 mA
Input circuit	Sink
Input filter	
Hardware	≤10 µs (channels 1 to 4) / ≤70 µs (channels 5 to 8)
Software	Default 0 ms, configurable between 0 and 25 ms in 0.2 ms intervals
Input resistance	Typ. 6 kΩ
Switching threshold	
Low	<5 VDC
High	>15 VDC
<b>Digital outputs</b>	
Variant	Current-sourcing FET
Switching voltage	I/O power supply minus residual voltage
Nominal output current	2 A
Total nominal current	8 A
Output circuit	Source
Output protection	Thermal shutdown in the event of overcurrent or short circuit, integrated protection for switching inductive loads, reverse polarity protection of the output power supply
Diagnostic status	Output monitoring with 10 ms delay
Leakage current when the output is switched off	5 µA
Switching on after overload shutdown	Approx. 10 ms (depends on the module temperature)
Residual voltage	<0.5 V at 2 A nominal current
Peak short-circuit current	<21 A
Switching delay	
0 → 1	<250 µs
1 → 0	<270 µs
Switching frequency	
Resistive load	Max. 100 Hz
Inductive load	See section "Switching inductive loads".
Braking voltage when switching off inductive loads	50 VDC
<b>Electrical properties</b>	
Electrical isolation	Bus isolated from POWERLINK and channel Channel not isolated from channel
<b>Operating conditions</b>	
Mounting orientation	
Any	Yes
Installation elevation above sea level	
0 to 2000 m	No limitation
>2000 m	Reduction of ambient temperature by 0.5°C per 100 m
Degree of protection per EN 60529	IP67
<b>Ambient conditions</b>	
Temperature	
Operation	-25 to 60°C
Derating	See section "Derating".
Storage	-40 to 85°C
Transport	-40 to 85°C
<b>Mechanical properties</b>	
Dimensions	
Width	53 mm
Height	85 mm
Depth	42 mm
Weight	200 g
Torque for connections	
M8	Max. 0.4 Nm
M12	Max. 0.6 Nm

Table 2: X67BC8331 - Technical data

- 1) For additional information, see section "Communication / POWERLINK / General information / Hardware - CN" in Automation Help.
- 2) The minimum cycle time specifies how far the bus cycle can be reduced without communication errors occurring.
- 3) The power consumption of the sensors and actuators connected to the module is not permitted to exceed 12 W.

## 2.2 LED status indicators

Figure	LED	Color	Status	Description
<p>Status indicator 1: Left: L/A IF1, Right: S/E</p> <p>Status indicator 2: Left: Green, Right: Red</p>	<b>Status indicator 1: Status indicator for POWERLINK bus controller</b>			
	L/A IF1 (Link/Active)	Green	On	The link to the remote station is established.
			Blinking	The link to the remote station is established. The LED blinks if POWERLINK activity is taking place on the bus.
	S/E <sup>1)</sup> (Status/Error)	Green/Red		LED states are described in section "Status/Error LED "S/E"" on page 4.
	<b>I/O LEDs</b>			
	1 - 8	Orange	-	Input/Output state of the corresponding channel
	<b>Status indicator 2: Status indicator for module functionality</b>			
	Left	Green	Off	No power to module
			Single flash	Mode RESET
			Blinking	Mode PREOPERATIONAL
			On	Mode RUN
	Right	Red	Off	Module not supplied with power or everything OK
			On	Error or reset state
			Single flash	Warning/Error on an I/O channel. Level monitoring for digital outputs has been triggered.
			Double flash	Supply voltage not within the valid range

1) This LED is a green/red dual LED.

### 2.2.1 Status/Error LED "S/E"

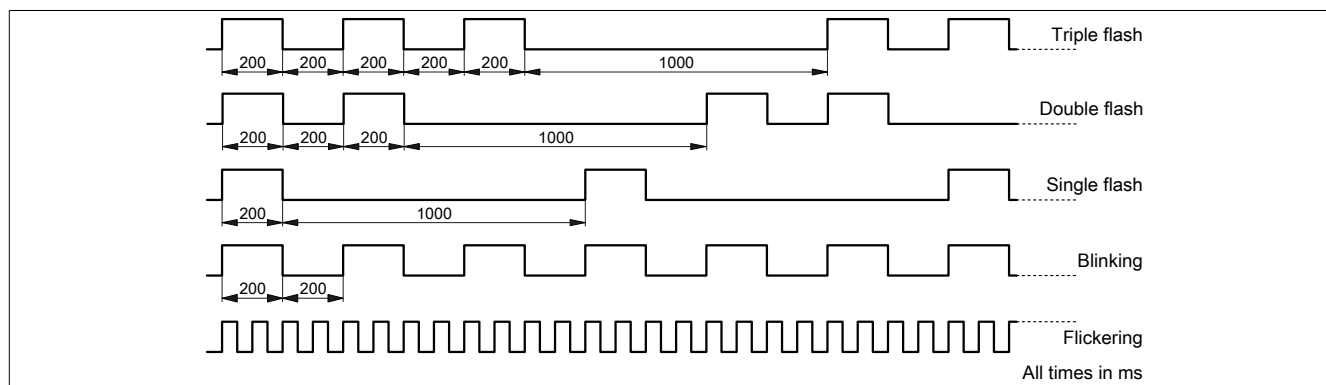
LED "Status/Error" is a green and red dual LED. The color green (status) is superimposed on the color red (error).

Color red - Error	Description
On	<p>The controlled node (CN) is in an error state (failed Ethernet frames, increased number of collisions on the network, etc.). If an error occurs in the following states, the red LED is superimposed by the green flashing LED:</p> <ul style="list-style-type: none"> <li>PRE_OPERATIONAL_1</li> <li>PRE_OPERATIONAL_2</li> <li>READY_TO_OPERATE</li> </ul> <p>Note:</p> <ul style="list-style-type: none"> <li>Several red blinking signals are displayed immediately after the device is switched on. This is not an error, however.</li> <li>The LED lights up red for CNs with set physical node number 0 that have not yet been assigned a node number via dynamic node allocation (DNA).</li> </ul>

Table 3: Status/Error LED lit red: LED indicating error state

Color green - Status	Description
Off	No power supply or mode NOT_ACTIVE. The controlled node (CN) is either not supplied with power or it is in state NOT_ACTIVE. The CN waits in this state for about 5 s after a restart. Communication is not possible with the CN. If no POWERLINK communication is detected during these 5 s, the CN changes to state BASIC_ETHERNET (flickering). If POWERLINK communication is detected before this time expires, however, the CN immediately changes to state PRE_OPERATIONAL_1.
Green flickering (approx. 10 Hz)	Mode BASIC_ETHERNET. The CN has not detected any POWERLINK communication. In this state, it is possible to communicate directly with the CN (e.g. with UDP, IP). If POWERLINK communication is detected in this state, the CN changes to state PRE_OPERATIONAL_1.
Single flash (approx. 1 Hz)	Mode PRE_OPERATIONAL_1. When operating on a POWERLINK V1 manager, the CN immediately changes to state PRE_OPERATIONAL_2. When operating on a POWERLINK V2 manager, the CN waits until an SoC frame is received and then changes to state PRE_OPERATIONAL_2.
Double flash (approx. 1 Hz)	Mode PRE_OPERATIONAL_2. The CN is normally configured by the manager in this state. It is then switched to state READY_TO_OPERATE by command (POWERLINK V2) or by setting flag "Data valid" in the output data (POWERLINK V1).
Triple flash (approx. 1 Hz)	Mode READY_TO_OPERATE. In a POWERLINK V1 network, the CN switches to state OPERATIONAL automatically as soon as input data is present. In a POWERLINK V2 network, the manager switches to state OPERATIONAL by command.
On	Mode OPERATIONAL. PDO mapping is active and cyclic data is evaluated.
Blinking (approx. 2.5 Hz)	Mode STOPPED. Output data is not being output, and no input data is being provided. It is only possible to switch to or leave this state after the manager has given the appropriate command.

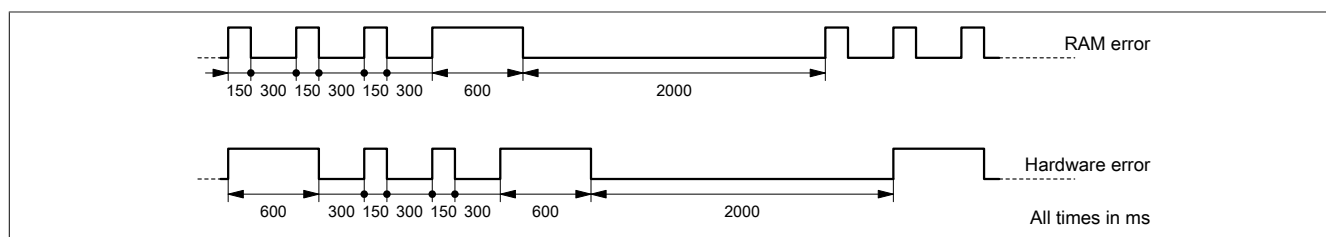
Table 4: Status/Error LED lit green: LED indicating operating state



## 2.2.2 System stop error codes

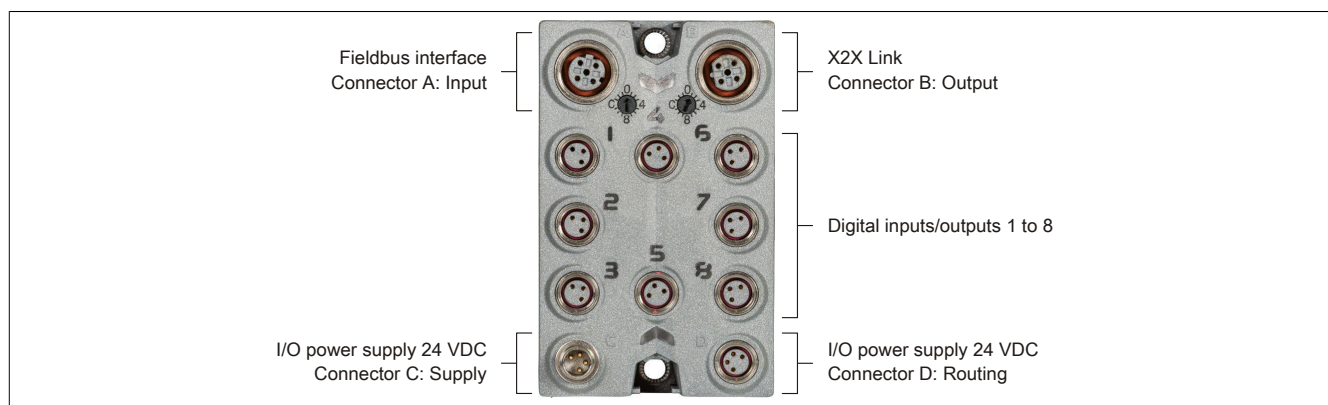
A system stop error can occur due to incorrect configuration or defective hardware.

The error code is indicated by LED "S/E" blinking red. The blinking signal of the error code consists of 4 switch-on phases with short (150 ms) or long (600 ms) duration. The error code is repeated every 2 seconds.



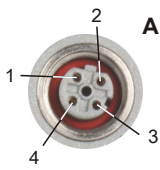
Error	Error description
<b>RAM error</b>	The device is defective and must be replaced.
<b>Hardware error</b>	The device or a system component is defective and must be replaced.

## 2.3 Operating and connection elements



### 2.3.1 Fieldbus interface

The module is connected to the network using pre-assembled cables. The connection is made using M12 circular connectors.

Connection	Pinout		
	Pin	Name	
	1	TXD	Transmit data
	2	RXD	Receive data
	3	TXD\	Transmit data\
	4	RXD\	Receive data\
Shield connection made via threaded insert in the module			
A → D-coded (female), input			

#### Information:

The color of the wires used in field-assembled cables for connecting to the fieldbus interface may deviate from the standard.

It is very important to ensure that the pinout is correct (see section "Accessories - POWERLINK cables" in the X67 user's manual).

#### 2.3.1.1 Wiring guidelines for bus controllers with Ethernet cable

Some X67 system bus controllers are based on Ethernet technology. POWERLINK cables offered by B&R can be used for wiring.

Order number	Connection type
X67CA0E41.xxxx	Attachment cables - RJ45 to M12
X67CA0E61.xxxx	Connection cables - M12 to M12

The following cabling guidelines must be observed:

- Use Cat 5 SFTP cables.
- Observe the bend radius of the cable (see the data sheet of the cable)

#### Information:

Using POWERLINK cables offered by B&R (X67CA0E61.xxxx and X67CA0E41.xxxx) meets product standard EN 61131-2.

The customer must implement additional measures in the event of further requirements.

### 2.3.2 POWERLINK node number




High Low

The node number for the POWERLINK node is set using the two number switches.

Switch position	Description
0x00	Only permitted when operating the POWERLINK node in DNA mode.
0x01 - 0xEF	Node number of the POWERLINK node. Operation as a controlled node (CN).
0xF0 - 0xFF	Reserved, switch position not permitted.

### 2.3.3 X2X Link

Additional modules are connected to the bus controller via X2X Link using pre-assembled cables. The connection is made using M12 circular connectors.

Connection	Pinout	
<div><b>B</b></div> 	Pin	Name
	1	X2X+
	2	X2X
	3	X2X <sub>L</sub>
	4	X2X\
	Shield via threaded insert in the module	
B → B-coded (female), output		

### 2.3.4 I/O power supply 24 VDC

The I/O power supply is connected via M8 connectors C and D. The power supply is fed via connector C (male). Connector D (female) is used to route the power supply to other modules.

The fieldbus / X2X Link power supply and I/O power supply are supplied separately via pins 1 and 2.

#### Information:

**The maximum permissible current for the I/O power supply is 8 A (4 A per pin)!**

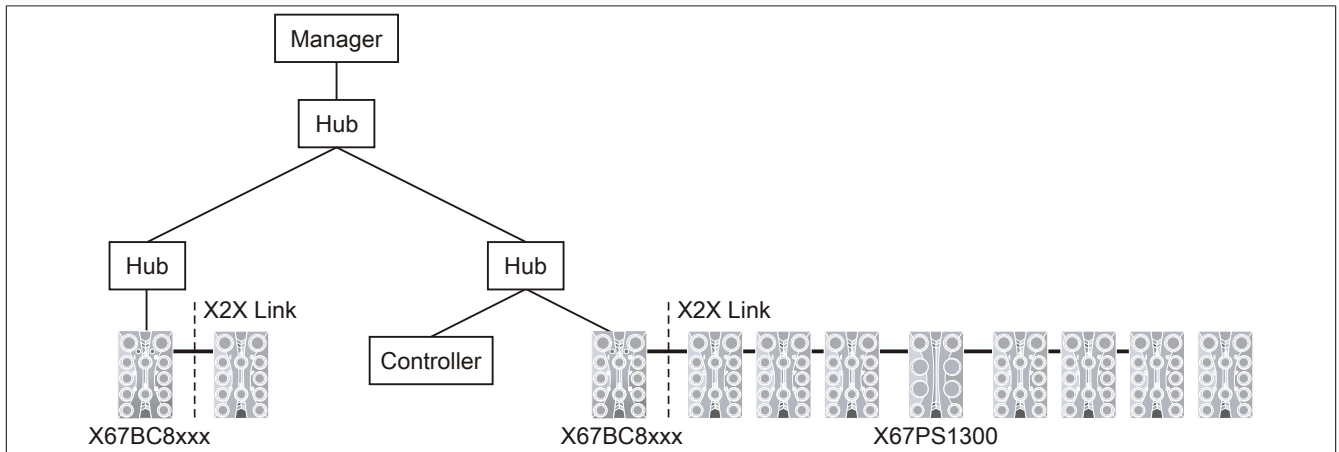
Connection	Pinout		
	Pin	Connector C (male)	Connector D (female)
	1	24 VDC fieldbus / X2X Link	24 VDC I/O
	2	24 VDC I/O	24 VDC I/O
	3	GND	GND
	4	GND	GND
C → Connector (male) in module, supply for I/O power supply D → Connector (female) in module, routing of I/O power supply			

#### Information:

**If the summation current of the outputs is >4 A, current must also be supplied via connector D, pin 2.**

## 2.4 System configuration

A digital mixed module is already integrated in the bus controller. Maximum 250 I/O modules can be connected to the bus controller.



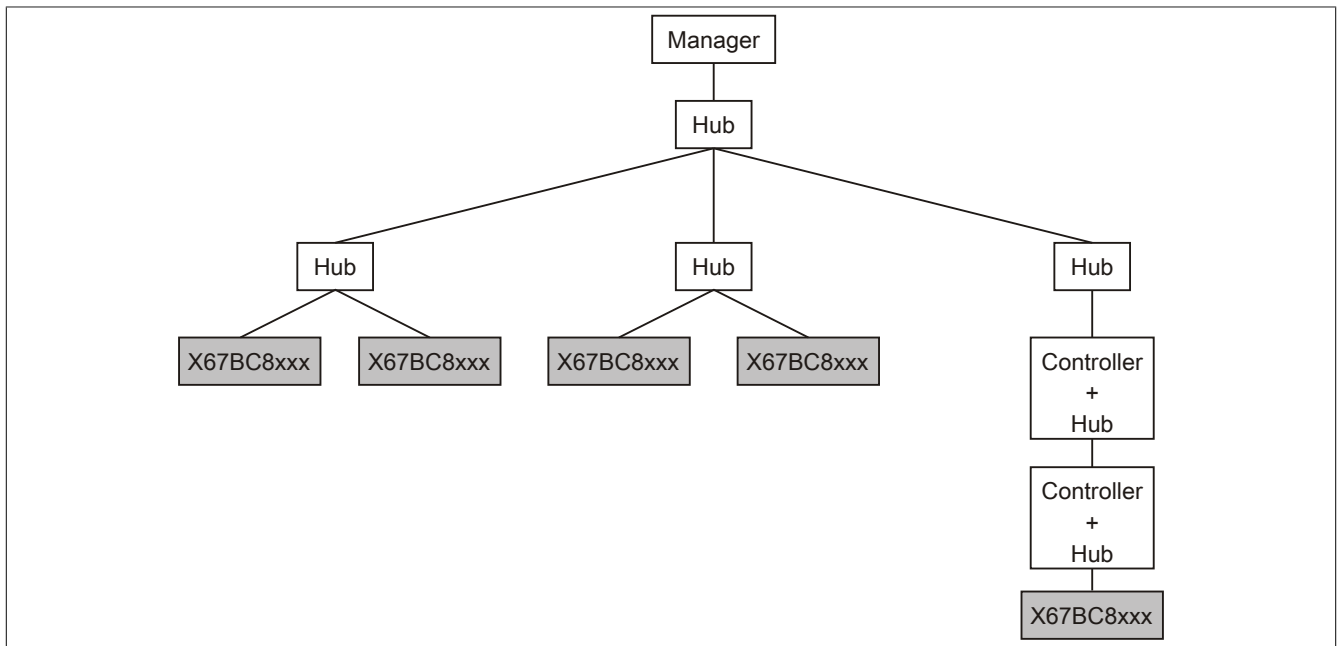
### Information:

3 W are made available from the bus controller for additional X67 modules or other modules based on X2X Link.

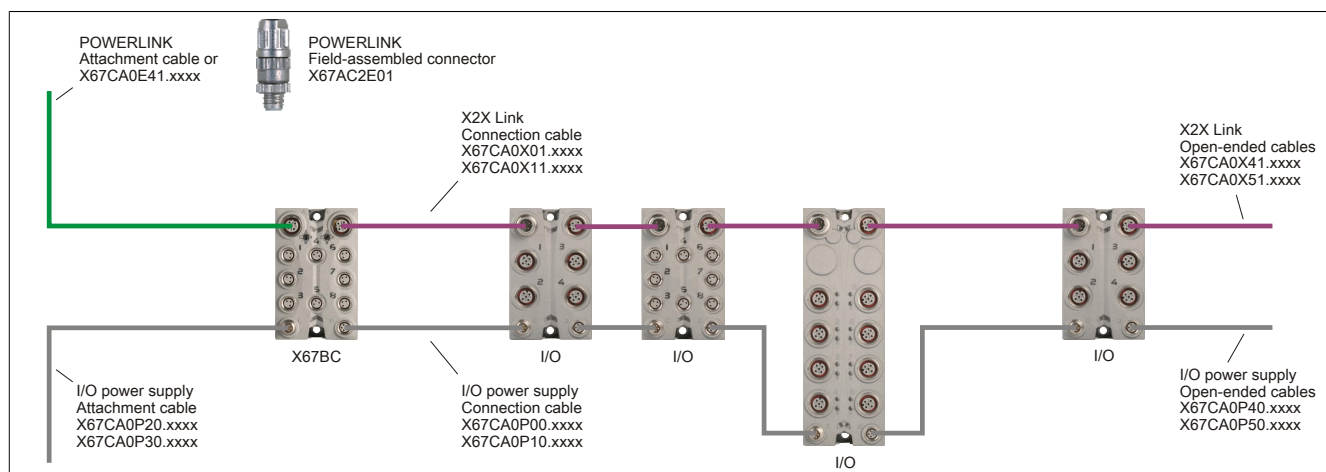
System supply module X67PS1300 is needed for additional power. This supply module provides 15 W for additional modules. It should be installed in the middle of the modules to be supplied with power.

#### 2.4.1 Integrating into a POWERLINK network

This bus controller is used in a tree or line structure as the last controller in each case.



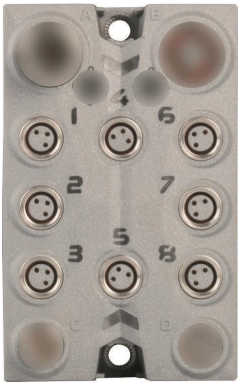
## 2.5 Required cables and connectors



3 Integrated digital mixed module

1 additional mixed module can be saved by the digital mixed module integrated in the bus controller.

3.1 Pinout

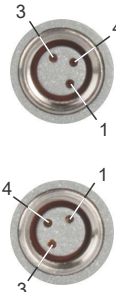


X1 to X8  
M8 ①

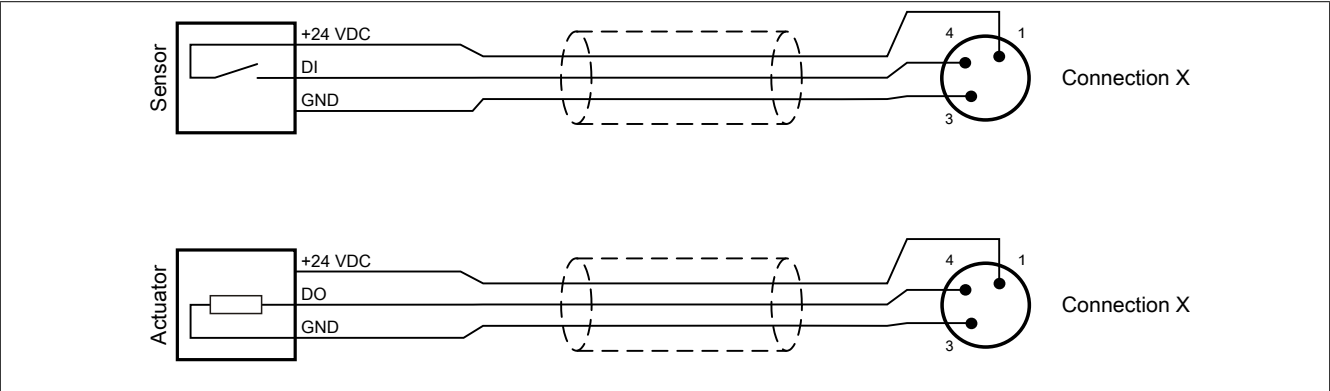
1	+24 VDC
3	GND
4	DI/DO x

- ① X67CA0D40.xxxx: M8 sensor cable, straight  
X67CA0D50.xxxx: M8 sensor cable, angled

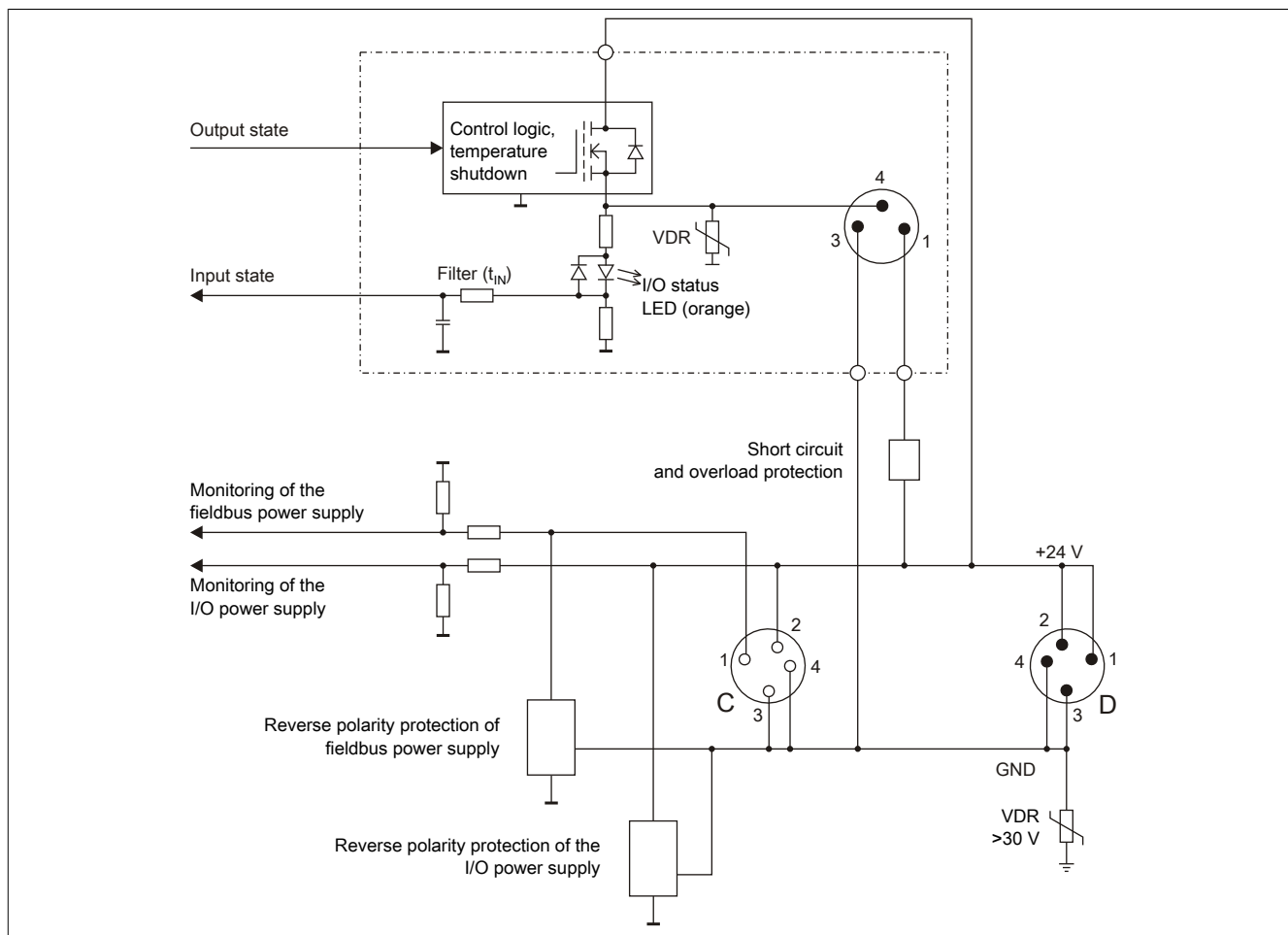
3.2 Connections X1 to X8

M8, 3-pin	Pinout	
	Pin	Name
	1	24 VDC sensor/actuator power supply <sup>1)</sup>
	3	GND
	4	Input/Output
1) The sensor/actuator power supply is not permitted to be external. Connections (female), input/output		

3.3 Connection examples



### 3.4 Input/Output circuit diagram



### 3.5 Derating / Operation with 2 A

The outputs of the module can be loaded with up to 2 A. With a summation current of 8 A, no more than 4 channels are operable at full load. To make the best use of the module, it is important to pay attention to the channel division and possible derating.

The correct channel division is important since the 8 outputs are divided between 2 output drivers. The channels operated at 2 A must therefore be divided equally between the two output drivers.

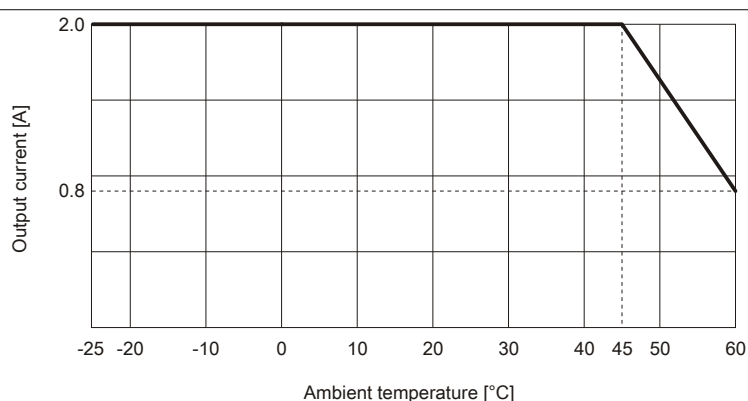
Output driver 1: Channels 1 to 4

Output driver 2: Channels 5 to 8

The following table provides an overview of the number of fully loaded channels, the resulting best division and possible derating.

Number of channels loaded with 2 A	Division	Derating
1	Any	No
2	1st channel with 2 A ... Channel no. 1 to 4 2nd channel with 2 A ... Channel no. 5 to 8	No
3	Assign only odd or even channel numbers. Examples: 1, 3, 5 2, 4, 6 3, 5, 7 4, 6, 8	Channels 1 and 3 Channels 2 and 4 Channels 5 and 7 Channels 6 and 8
4	Assign only odd or even channel numbers. Possible divisions: 1, 3, 5, 7 2, 4, 6, 8	For each channel For each channel

Derating when 3 or 4 channels are operated with 2 A:



## 4 Function description

### 4.1 POWERLINK

POWERLINK is an Ethernet-based, real-time capable fieldbus. POWERLINK extends the IEEE 802.3 Ethernet standard by a deterministic access method and also defines a CANopen-compatible fieldbus interface. POWERLINK distinguishes between process and service data in the same way as CANopen. Process data (PDO) is exchanged cyclically in the cyclic phase, while service data (SDO) is transferred acyclically. Service data objects are transmitted in the acyclic phases of POWERLINK using a connection-oriented protocol. The cyclic transfer of data in PDOs is enabled by "mapping".

For additional information, see [POWERLINK bus controller user's manual](#) and [www.br-automation.com/en/technologies/powerlink](http://www.br-automation.com/en/technologies/powerlink).

### 4.2 Digital inputs

The module is equipped with 8 digital channels that can be configured as digital inputs.

#### Information:

The register is described in ["I/O masks 1 to 8" on page 15](#).

#### 4.2.1 Recording the input state

##### Unfiltered

The input state is collected with a fixed offset to the network cycle and transferred in the same cycle.

##### Filtered

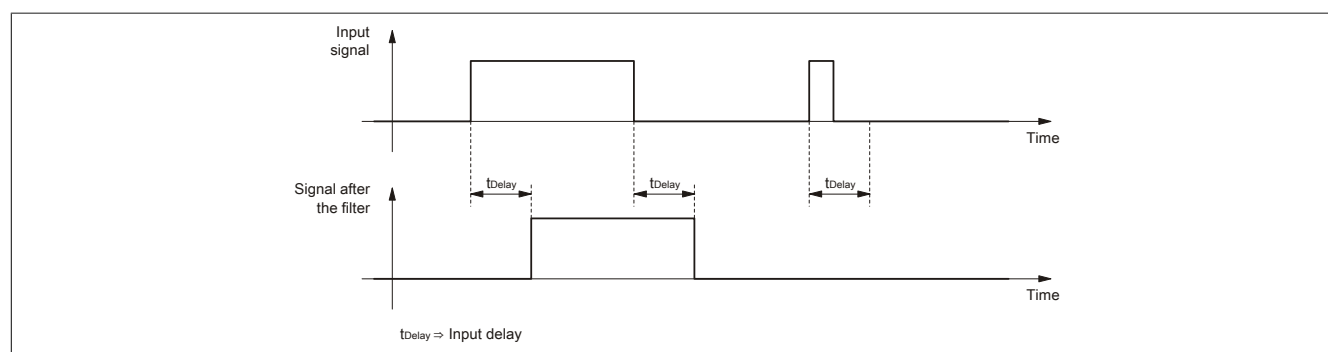
The filtered state is collected with a fixed offset to the network cycle and transferred in the same cycle. Filtering takes place asynchronously to the network in multiples of 200  $\mu$ s with a network-related jitter of up to 50  $\mu$ s.

#### Information:

The register is described in ["Input state of digital inputs 1 to 8" on page 16](#).

#### 4.2.2 Input filter

An input filter is available for each input. Disturbance pulses that are shorter than the input delay are suppressed by the input filter.



The input delay can be set in steps of 100  $\mu$ s. It makes sense, however, to enter values in steps of 2 since the input signals are sampled in an interval of 200  $\mu$ s.

Values	Filter
0	No software filter
2	0.2 ms
...	...
250	25 ms - Higher values are limited to this value.

#### Information:

The register is described in ["Input filter" on page 15](#).

## 4.3 Digital outputs

The module is equipped with 8 digital channels that can be configured as digital outputs.

### Information:

The register is described in ["I/O masks 1 to 8" on page 15](#).

### 4.3.1 Monitoring status of the outputs

On the module, the output states of the outputs are compared to the target states. The control of the output driver is used for the target state.

A change in the output state resets monitoring for that output. The status of each individual channel can be read out. A change in the monitoring status is actively transmitted as an error message.

Supervision status	Description
0	Digital output channel: No error
1	Digital output channel: Short circuit or overload

### Information:

The register is described in ["Monitoring status of the digital outputs" on page 17](#).

## 5 Commissioning

### 5.1 SGx target systems

#### SG3

This module is not supported on SG3 target systems.

#### SG4

The module comes with preinstalled firmware. The firmware is also part of the Automation Runtime operating system for the PLC. With different versions, the Automation Runtime firmware is loaded onto the module.

Current firmware is made available automatically by updating Automation Runtime.

## 6 Register description

### 6.1 General data points

In addition to the registers described in the register description, the module has additional general data points. These are not module-specific but contain general information such as serial number and hardware variant.

General data points are described in section "Additional information - General data points" in the X67 system user's manual.

### 6.2 Function model 0 - Standard

Register	Name	Data type	Read		Write	
			Cyclic	Acyclic	Cyclic	Acyclic
Configuration						
16	ConfigIOMask01	USINT				•
18	ConfigOutput03 (input filter)	USINT				•
Communication						
0	Input state of digital inputs 1 to 8	USINT	•			
	DigitalInput01	Bit 0				
	...	...				
	DigitalInput08	Bit 7				
2	Switching state of digital outputs 1 to 8	USINT			•	
	DigitalOutput01	Bit 0				
	...	...				
	DigitalOutput08	Bit 7				
30	Status of digital outputs 1 to 8	USINT	•			
	StatusDigitalOutput01	Bit 0				
	...	...				
	StatusDigitalOutput08	Bit 7				

### 6.3 Configuration

#### 6.3.1 I/O masks 1 to 8

Name:

ConfigIOMask01

Channels can be configured as inputs/outputs in this register. It also determines whether output monitoring or filtering is applied to the channels. Outputs are monitored but not filtered.

Data type	Values
USINT	See the bit structure.

Bit structure:

Bit	Description	Value	Information
0	Channel 1 configured as input/output	0	Configured as input
		1	Configured as output
...	...	...	...
7	Channel 8 configured as input/output	0	Configured as input
		1	Configured as output

#### 6.3.2 Input filter

Name:

ConfigOutput03

The filter value for all digital inputs can be configured in this register.

The filter value can be configured in steps of 100  $\mu$ s. It makes sense, however, to enter values in steps of 2 since the input signals are sampled in an interval of 200  $\mu$ s.

Data type	Values	Filter
USINT	0	No software filter
	2	0.2 ms
	...	...
	250	25 ms - Higher values are limited to this value.

## 6.4 Communication

### 6.4.1 Digital inputs

#### 6.4.1.1 Input state of digital inputs 1 to 8

Name:

DigitalInput01 to DigitalInput08

This register contains the input state of digital inputs 1 to 8.

Data type	Values
USINT	See the bit structure.

Bit structure:

Bit	Name	Value	Information
0	DigitalInput01	0 or 1	Input state - Digital input 1
...		...	
7	DigitalInput08	0 or 1	Input state - Digital input 8

### 6.4.2 Digital outputs

The output state is transferred to the output channels with a fixed offset in relation to the network cycle (SyncOut).

#### 6.4.2.1 Switching state of digital outputs 1 to 8

Name:

DigitalOutput01 to DigitalOutput08

This register stores the switching state of digital outputs 1 to 8.

Data type	Values
USINT	See the bit structure.

Bit structure:

Bit	Name	Value	Information
0	DigitalOutput01	0	Digital output 01 reset
		1	Digital output 01 set
...		...	
7	DigitalOutput08	0	Digital output 08 reset
		1	Digital output 08 set

### 6.4.3 Monitoring status of the digital outputs

On the module, the output states of the outputs are compared to the target states.

#### 6.4.3.1 Status of digital outputs 1 to 8

Name:

StatusDigitalOutput01 to StatusDigitalOutput08

This register contains the state of digital outputs 1 to 8.

Data type	Values
USINT	See the bit structure.

Bit structure:

Bit	Name	Value	Information
0	StatusDigitalOutput01	0	Channel 01: No error
		1	Channel 01: Short circuit or overload
...		...	
7	StatusDigitalOutput08	0	Channel 08: No error
		1	Channel 08: Short circuit or overload

### 6.5 Minimum I/O update time

The minimum I/O update time specifies how far the bus cycle can be reduced so that an I/O update is performed in each cycle.

Minimum I/O update time	
Without filtering	150 µs
With filtering	200 µs

### 6.6 Minimum cycle time

The minimum cycle time specifies how far the bus cycle can be reduced without communication errors occurring. It is important to note that very fast cycles reduce the idle time available for handling monitoring, diagnostics and acyclic commands.

Minimum cycle time	
Without filtering	150 µs
With filtering	200 µs